IN THE UNITED STATES PATEN	T AND TRADEMARK OFFICE
Applicant: P Alexander A. Maltsev et al.	
Title: SYSTEM AND METHOD FOR CHAN	INELIZATION RECOGNITION IN A WIDEBAND
Docks No.: _, 884.A53US1	Serial No.: 10/728,476
Filed: December 4, 2003	Due Date: N/A
Examiner: Unknown	Group Art Unit: 2631
Mail Stop Amendment	
Commissioner for Patents	
P.O. Box 1450	
Alexandria, VA 22313-1450	
We are transmitting herewith the following attached item	s (as indicated with an "X"):
$\frac{X}{X}$ A return postcard. An Information Disclosure Statement (2 pgs.), Form	1 1449 (1 pg.), and copies of 2 cited documents.
If not provided for in a separate paper filed herewith, Please con sufficient number of months to enter these papers and please characteristics. Account No. 19-0743.	sider this a PETITION FOR EXTENSION OF TIME for arge any additional fees or credit overpayment to Deposit
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Customer Number 21186	By: <u>Onn M. McCaeka.</u> Atty: Ann M. McCrackin Reg. No. 42,858
CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby cert States Postal Service with sufficient postage as first class mail, in an Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 22 d	envelope addressed to: Mail Stop Amendment, Commissioner for
<u>KACIA LEE</u>	Vacia Lee
Name	Signaturė

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. (GENERAL)

O 1 P ESAN 10/728,476

JUL 2 6 2004

**PATENT** 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Alexander A. Maltsev et al.

Examiner:

Unknown

Sérial No.:

oplicant:

10/728,476

Group Art Unit:

2631

Filed:

December 04, 2003

Docket:

884.A53US1

Title:

SYSTEM AND METHOD FOR CHANNELIZATION RECOGNITION IN A

WIDEBAND COMMUNICATION SYSTEM

Assignee:

**Intel Corporation** 

Customer No:

21186

## INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No: 10/728,476

Filing Date: December 04, 2003

Dkt: 884.A53US1 (INTEL)

Title: SYSTEM AND METHOD FOR CHANNELIZATION RECOGNITION IN A WIDEBAND COMMUNICATION SYSTEM

Assignee: Intel Corporation

The present application is either a U.S. national patent application filed after June 30, 2003 or an international application that entered the national stage under 35 U.S.C. § 371 after June 30, 2003. Thus, Applicant believes that the U.S. Patent & Trademark Office has waived the requirement under 37 C.F.R. 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication. The waiver is provided in a pre-OG notice from the U.S. Patent & Trademark Office entitled "Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003" and dated July 11, 2003. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

ALEXANDER A. MALTSEV ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. Attorneys for Intel Corporation P.O. Box 2938
Minneapolis, Minnesota 55402

(612) 349-9592

Date 1, 1, 2004

Ann M. McCrackin

Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 22 day of July, 2004.

Name KACIA LEE

Signature Lee

PTO/SB/08A(10-01)
Approved for use through 10/31/2002. OMB 651-0031
US Patent & Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for form 1449A/PTO Complete if Known INFORMATION DISCLOSURE 10/728,476 **Application Number** STATEMENT BY APPLICANT December 4, 2003 (Use as many sheets as necessary) **Filing Date** Maltsev, Alexander **First Named Inventor** JUL 2 6 2004. **Group Art Unit** 2631 Unknown **Examiner Name** Attorney Docket No: 884.A53US1 Sheet 1 of 1

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>

	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Cite Initials* No 1		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the ite (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
		BANGERTER, BOYD, "High-Throughput Wireless LAN Air Interface", Intel Technology Journal: Wireless Technologies, 7(3), Available at http://developer.intel.com/technology/itj/index.htm, (August 19, 2003), 47-57	
•		KHUN-JUSH, JAMSHID, et al., "Structure and performance of the HIPERLAN/2 physical layer", VTC 1999 - Fall. IEEE VTS 50th Vehicular Technology Conference, Volume 5, (September 19-27,), 2667-2671	